HEWLETT PACKARD

Real-Time Emulator
Intel® 8048/8049 Family

**MODEL 64260A
OPTION 262**

Description

Model 64260A Option 262 Emulator provides functionally transparent, real-time emulation of the 8048/8049 family of microprocessors. Each emulator consists of two emulation control boards and an emulator pod assembly. High-speed emulation memory, contained on one of the emulation control boards, allows emulation of the processor at specified speeds. The 64260A Emulation System is compatible with target systems using processors that meet the specifications of Intel 8035, 8039, 8048, and 8049 microprocessors.

Features

Software control of emulation/target system memory mapping.

 Map 1k-byte blocks of the 4k-byte program address space into either user or emulation memory.

 Map 256 bytes of external data memory address space into either user or emulation memory.

Symbolic debugging capability.

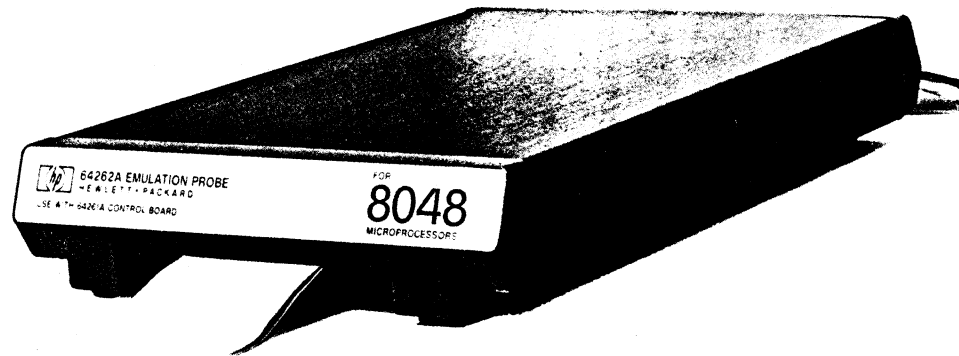Read and modify memory, registers, and ports.

Real-time emulation at specified speeds.

Simulated I/O for emulator access to HP 64000 system resources: disc files, printer, development station keyboard and display, and RS-232 port.

Fast downloading of programs into emulation memory.

Powerful logic analysis for complex program tracing.

®Registered, Intel Corporation

Characteristics

ELECTRICAL
Maximum Clock Speeds: 11 MHz with memory assigned to target or emulation memory. Specific timing for some signals may differ from the actual processor timing. Refer to emulation manual supplement for more details.
Clock Frequency: external crystal, 3.5 MHz to 11 MHz; external source, 1 MHz to 11 MHz; internal source, 6 MHz.
Emulation Pod to Target System Interface: low power Schottky TTL levels with capacitance of approx 20 pF, except clock inputs which meet Intel 8049 specifications plus capacitance of approx 20 pF. Ports P1 and P2 have an additional 10 kΩ pull-up resistor.
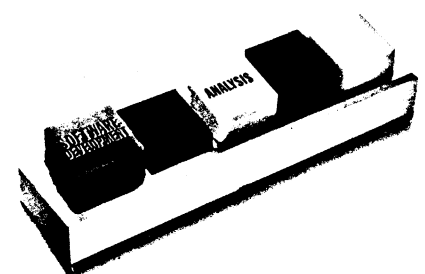
PHYSICAL
Cable Length: development station to emulation pod, approx 1.5 m (5 ft); emulation pod to target system interface, approx 305 mm (1 ft).

ENVIRONMENTAL
Temperature: operating, 0°C to +40°C (+32°F to +104°F); nonoperating, -40°C to +75°C (-40°F to +167°F); operating survival, -20°C to +50°C (-4°F to +122°F).
Altitude: operating, 4600 m (15 000 ft); nonoperating, 15 300 m (50 000 ft).
Relative Humidity: 5% to 80%.

Ordering Information

FACTORY INSTALLED
Model 64001S Logic Development System
  Option 262: 8048/8049 Family Emulation System
  Option 300: Logic Analyzer.

USER INSTALLED
Model 64260A 8048/8049 Family Emulation System
  Option 262: 8048/8049 Emulation System and Assembler
Model 64261A 8048/8049 Emulator Control Board
Model 64262A 8048/8049 Emulator Pod
Model 64300A Logic Analyzer
Model 64846A 8048/8049 Family Assembler/Linker
Model 64846B 8048/8049 Family Emulation Control Software

Your HP Field Engineer can help you determine the best
configuration to meet your needs.